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NOV 28 2005

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08/26/2005

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11/29/2005 TBESHAW2 00000035 10791599

01 FC:1501 1400.00 OP
 02 FC:1504 300.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/91,599	03/01/2004	How-Chuan Wu	67,200-1103	2036

TITLE OF INVENTION: METHOD FOR INTEGRATED MANUFACTURING OF SPLIT GATE FLASH MEMORY WITH HIGH VOLTAGE MOSFETS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1100	\$300	\$1700	11/28/2005
EXAMINER		ART UNIT	CLASS-SUBCLASS		
LEE, CALVIN		2818	438-209000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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R. W. Tung

2.

3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Taiwan Semiconductor Manufacturing Co., Ltd. Hsin Chu, Taiwan

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Randy W. TungRegistration No. 31,311

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